TECHNOLOGCE H		SAMRAT ASHOK TECHNOLOGICAL INSTITUTE VIDISHA (M.P.) (A Govt. Aided UGC Autonomous & NAAC Accredited Institute Affiliated to RGPV Bhopal)														
		Scheme of Examination (Semester-I)														
			for Batch Admitted in session - 2024-25 and ONWards													
		Bachelor of Technology (B. Tech.)- Electronics and Communication Engineering														
Subject Code	Subject Category		Maximum Marks Allotted Contact Hrs.													
		Subject Name	Theory				Practical			Total				Total		
			ES	MS	Assignment	Quiz	ES	LW	Quiz	Marks	L	Т	Р	Credits		
PYB101	BSC	Applied Physics	60	20	10	10	30	10	10	150	3	0	2	4		
ECA101	ESC	Basic Electronics	60	20	10	10	30	10	10	150	3	0	2	4		
ITC 101	ITC	Python Programming	60	20	10	10	30	10	10	150	3	0	2	4		
ECA104	ESC	Fundamental Of Instrumentation	60	20	10	10				100	3	0	0	3		
MAB101	BSC	Linear Algebra and Calculus	60	20	10	10				100	3	1	0	4		
ILC100	ILC	Extracurricular Activities	It is a one credit per vear activity endorse in eight semester marksheet													
Total			300	100	50	50	90	30	30	650	15	1	6	19		
MAC101	MAC*	Universal Human Values					60	20	20	100	0	0	2	Grade		
MST: Min	nimum two m	id semester tests to be conducted dur	ng Semes	ster, MAC	Mandatory co	urses class	es will be	conducted	l in off ho	urs (Week	ends)	•	•			

MAC	Induction Program	Non-Credit
HEC	NSS/NCC/NSO	Non-Credit

Abbreviations: ES -End Semester, MS- Mid Semester, LW- Laboratory Work/Assignment. (L: Lecture, T: Tutorial, P: Practical) BSC- Basic Science Course, ESC- Engineering Science Course, HSMC- Humanities Science and Management Course, MAC- Mandatory, Audit Course, AC- Audit Course, HEC- Holistic Education Course, ITC- Information Technology Course, ILC-Institute Level Course, DC- Department Course, DE-Department Elective, OC- Open Course, DLC- Department Laboratory, PROJ- Project Work

Alinedi Van - Stand Stan