



SAMRAT ASHOK TECHNOLOGICAL INSTITUTE VIDISHA (M.P.)
(A Govt. Aided UGC Autonomous & NAAC Accredited Institute Affiliated to RGPV Bhopal)

Scheme of Examination (Semester-V) (B.Tech.) - Electronics & Communication Engg.
for Batch Admitted in session - 2022-23

Subject Code	Subject Category	Subject Name	Maximum Marks Allotted								Contact Hrs.			Total Credits
			Theory				Practical			Total Marks	L	T	P	
			ES	MS	Assignment	Quiz	ES	LW	Quiz					
EC-501	DC	Digital Signal Processing	60	20	10	10	30	10	10	150	3	0	2	4
EC-502	DC	Microprocessor and Microcontroller	60	20	10	10	30	10	10	150	3	0	2	4
EC-503	DC	Microwave theory	60	20	10	10	30	10	10	150	3	0	2	4
EC-504	DE	DE-1	60	20	10	10	--	--	--	100	3	1	0	4
EC-505	OC	OC-3	60	20	10	10	--	--	--	100	3	0	0	3
EC506	DLC	Simulation Lab II(Labview)	--	--	--	--	30	10	10	50	0	0	4	2
	ILC	Internship-II (60 Hrs) Institute Level (Evaluation)	--	--	--	--		50	--	50	-	-	4	2
	ILC	Extracurricular Activities	It is a one credit per year activity to be endorsed in eight semester mark sheet											
Total			300	100	50	50	120	90	40		15	1	14	23

	DE -1	OC-3
A	Wireless Communication	Signal and Systems
B	Computer Architecture	Microprocessor and Microcontroller
C	Power Electronics	Industrial Electronics
		VLSI Circuit Design

Abbreviations: ES -End Semester, MS- Mid Semester, LW- Laboratory Work/Assignment. (L: Lecture, T: Tutorial, P: Practical)
BSC- Basic Science Course, ESC- Engineering Science Course, HSMC- Humanities Science and Management Course, MAC- Mandatory, Audit Course, AC- Audit Course, HEC- Holistic Education Courses: NSS/NCC/NSO, ITC- Information Technology Course, ILC-Institute Level Course, DC- Department Course, DE-Department Elective, OC- Open Course, DLC- Department Laboratory, PROJ- Project Work, VA-Value Added Course