



SAMRAT ASHOK TECHNOLOGICAL INSTITUTE VIDISHA (M.P.)
Department of Computer Science and Engineering
Scheme of Examination
Bachelor of Technology (B.Tech) in Computer Science and Engineering

Semester-V

S. No.	Subject Code	Category	Subject Name & Title	Maximum Marks Allotted						Contact Hrs.			Total Credits
				Theory Slot			Practical Slot			L	T	P	
				End Sem	Mid Sem. Exam	Quiz, Assignm ent	End Sem.	Term work Lab Work & Sessional	Total Marks				
1	CSE-1851	DC	Operating System	70	20	10	30	20	150	3	-	2	4
2	CSE-1852	DC	Compiler Design	70	20	10	30	20	150	3	-	2	4
3	CSE-1853	DC	Analog and Digital Communication	70	20	10	30	20	150	3	-	2	4
4	CSE-1854	DC	Computer Graphics And Multi Media	70	20	10	-	-	100	3	1	-	4
5	CSE-1855	OC	OC-1	70	20	10	-	-	100	3	-	-	3
6	CSE-1856	DLC	Linux and Shell Programming	-	-	-	30	20	50	-	-	2	1
7	CSE-1857	DLC	Internship – II (Evaluation) (personality development)	-	-	-	50	-	50	-	-	4	2
8		DLC	60 hrs Internship in Industry or elsewhere Internship-III	To be evaluated in VII Semester									
			Total	350	100	50	170	80	750	15	1	12	22
9		MC	Disaster Management										
10		SL	MOOCs	-	-	-	-	-	-	-	-	-	-
11		NLC	Participation & Winning in National level competition										

MST: Minimum two mid semester tests to be conducted during Semester

L: Lecture T: Tutorial P: Practical

OC-1	
A	Principle of Programming Language
B	Information Storage and Management
C	Data Communication

Shankar
4/06/19
Prof. S. Chugh

Ajay
Prof. Ajay Kumar Goyal

S
Mr. S Jain

Motwani
04.6.19
Dr. M Motwani

Shrivastava
Dr. S K Shrivastava