



SAMRAT ASHOK TECHNOLOGICAL INSTITUTE VIDISHA (M.P.)
Department of Computer Science and Engineering
Scheme of Examination
Bachelor of Technology (B.Tech) in Computer Science and Engineering

Semester-VII

S. No.	Subject Code	Category	Subject Name & Title	Maximum Marks Allotted						Contact Hrs.			Total Credits
				Theory Slot			Practical Slot			L	T	P	
				End Sem	Mid Sem. Exam	Quiz, Assignm ent	End Sem.	Term work Lab Work & Sessional	Total Marks				
1	CSE-1871	DE	DE-4	70	20	10	-	-	100	3	1	-	4
2	CSE-1872	DE	DE-5	70	20	10	-	-	100	3	1	-	4
3	CSE-1873	DE	DE-6	70	20	10	-	-	100	3	-	-	3
4	CSE-1874	OC	OC-3	70	20	10	-	-	100	3	-	-	3
5	CSE-1875	OC	OC-4	70	20	10	-	-	100	3	-	-	3
6	CSE-1876	DLC	IT workshop (Sci Lab/MATLAB)	-	-	-	30	20	50	-	-	2	1
7	CSE-1877	DLC	Internship-III (Evaluation) (personality development)	-	-	-	50	-	50	-	-	4	2
8	CSE-1878	DLC	Major Project Prelim	-	-	-	100	50	150	-	-	4	2
			Total	350	100	50	160	90	750	15	2	10	22
9		NLC	Participation & Winning in National level competition	-	-	-	-	-	-	-	-	-	-
10		MC	Intellectual property Right(IPR)	-	-	-	-	-	-	-	-	-	-
11		SL	MOOCs	-	-	-	-	-	-	-	-	-	-

MST: Minimum two mid semester tests to be conducted during Semester

L: Lecture

T: Tutorial

P: Practical

	DE-4	DE-5	DE-6	OC-3	OC-4
A	OOAD	Cloud Computing	Data Ware Housing and Data Mining	Artificial Intelligence	Machine Learning
B	Embedded System	Digital Image Processing	Natural Language Processing	Human Computer Interaction	Optimization Techniques
C	Parallel Computing	Wireless Network	Data Compression	ERP	Robotics

Shauq
24/11/19
Prof. S. Chugh

Ajay
Prof. Ajay Kumar Goyal

Sain
Mr. S Jain

M.
Motwani
04.6.19
Dr. M Motwani

S K Shrivastava
04/11/19
Dr. S K Shrivastava