

SAMRAT ASHOK TECHNOLOGICAL INSTITUTE, VIDISHA (M.P)
(An Autonomous Engg. College)

Session :- July – Dec. 2024

Electrical Engineering Department

TIME TABLE

BE V sem. Electrical Engg. In force from :- 15 July 2024 (Revised on 01.08.2024)

Room No :

208

Day's	I 10:30 - 11:30	II 11:30- 12:30	III 12:30 - 1:30	1:30 To 2:30 PM	IV 2:30-3:30	V 3:30-4:30	IV 4:30-5:30	Subject
Monday	OE EE 505 ST	EE 502 MJ	EE 501 SS	L u n c h B R E A K	----- EE 501 Lab----- SS/DJ RS		507 SS	EE 501 Control System EE 502 Power Electronics EE 503 Power system-II EE 504 Microprocessor EE 505 OE III EE 506 Machine Design Lab EE 507 Internship – II (Evaluation) (personality Development)
Tuesday	EE 503 ST	EE 504 JT	EE 501 SS		----- EE 504 Lab----- JT/SS JK		507 SS	
Wednesday	OE EE 505 ST	EE 502 MJ	EE 504 JT		----- EE 502 Lab----- MJ/DJ RS		507 SS	
Thursday	EE 504 JT	EE 501 SS	EE 503 ST		----- EE 506 Lab----- SST/AS JK		507 SS	
Friday	OE EE 505 ST	EE 502 MJ	EE 503 ST		----- EE 506 Lab----- SST/AS RS		507 SS	

I/c Time Table

HOD, EE

Copy to : 1. Dean Academic
2. Deptt. Notice Board
3. Elect. Engg. Deptt.