



SAMRAT ASHOK TECHNOLOGICAL INSTITUTE, VIDISHA
(A Govt. Aided UGC Autonomous & NAAC Accredited Institute Affiliated to RGPV, Bhopal)
Department of Electronics Engineering

Program: Electronics & Communication Engineering

BoS Meeting

(BoS Meeting Scheduled on 20 December 2024)

- Date and Time of BoS** 20.12.2024, 03:00PM Onwards.
- Held online at:** <https://meet.google.com/bik-bvqr-mgu>
- Board of Studies members**
- A. Dr. Ashutosh Datar, Head (Dept. of Electronics Engg.) & Chairman
 - B. The entire faculty of each specialization:
 - 1. Dr. Neelesh Mehra, Course Coordinator (E&C)
 - 2. Dr. D.K Shakya, Asst. Professor
 - 3. Prof. ML Jatav, Asst. Professor
 - 4. Prof. Bharti Mehra, Asst. Professor
 - 5. Dr. Suchi Mishra, Asst. Professor
 - 6. Dr. Sweety Jain, Asst. Professor
 - 7. Dr. Abhishek Jain, Asst. Professor
 - 8. Prof. Niraj Dhakad, Asst. Professor
 - C. Two subject experts from outside the university
 - 1. Dr. Aditya Trivedi, Professor, IITM, Gwalior
 - 2. Dr. Vandana Vikas Thakre, Professor, Department of Electronics, MITS Gwalior
 - D. One representative from Industry/Corporate sector, allied/area
Mr. Ashutosh Patrikar, Senior Manager, EY, Pune
 - E. Postgraduate Meritorious Alumnus
Dr. Anand Agrawal, Assistant Professor, Department of ECE, IIT Kota
 - F. One expert nominated by Vice Chancellor, RGPV.
Dr. Prabhat Patel, Professor, UIT, RGPV, Bhopal

Comment: Dr. Prabhat Patel and Dr. Aditya Trivedi did not join the meeting.



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BoS Agenda Items & Resolutions

Item 1	To confirm the minutes of previous BoS meeting held in the month of June 2024
Resolution	As per the agenda point no.1, the minutes of the last Board of Studies meeting held on 12 th June 2024 have been confirmed and approved.
Item 2	To review, prepare, finalize and recommend the Scheme & Syllabi (along with the Course Outcomes) of VI semester B.Tech. Programme in Electronics and Communication Engineering (for the batch admitted in 2022-23 Session)
Resolution	The scheme and syllabus for VI semester B.Tech programme has been discussed and finalized. Link for the scheme & syllabus is as follows: https://www.satiengg.in/images/Electronics%20Department/BoS%20June24/Scheme%202024/VI%20SemScheme2024.pdf https://www.satiengg.in/images/Electronics%20Department/BoS%20Dec%202024/6%20sem%20syllabus.pdf
Item 3	To discuss the Scheme of VII and VIII semester B. Tech. Programme in Electronics and Communication Engineering (for the batch admitted in 2022-23 Session)
Resolution	The scheme and syllabus for VII and VIII semester B. Tech programme as per the format proposed by the Dean Academic office was discussed.
Item 4	To discuss and finalize the open course offered by department for other branch students in B.Tech. VI Semester. (for the batch admitted in 2022-23 Session)
Resolution	The list of Open Elective (OE) Courses of 3 credits proposed for the other department students in VI semester along with their syllabus was presented and approved by BOS committee.
Item 5	To review, prepare, finalize and recommend the list of experiments/ Lab manual and skill based mini projects for various laboratory courses to be offered in VI semester B.Tech. Programme in Electronics and Communication Engineering (for the batch admitted in 2022-23).
Resolution	The suggestive list of experiments mentioned along with detailed syllabus of different subjects of B. Tech. VI Semester (for the batch admitted in 2022-23) are discussed and approved.



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Item 6	To review, recommend and finalize the changes made in Scheme & Syllabi (along with the Course Outcomes) of I st to IV th Semester B. Tech. Programme in Electronics and Communication Engineering (for the upcoming batches I st and II nd year onwards (2023-24 onwards batch))
Resolution	Scheme & Syllabi (along with the Course Outcomes) of I st to IV th semester B. Tech. Programme (for the upcoming batches I year and II year 2023-24 onwards (admitted after 2022-23 Session)) are discussed and finalized. Syllabus of EC-405 control system and EC-404 Digital Communication courses are induced in fourth semester. Syllabus and proposed scheme are presented and approved unanimously by the board members. Link for the scheme is as follows: https://www.satiengg.in/images/Electronics%20Department/BoS%20June24/Scheme%202024/IV%20SemScheme2024%20revise.pdf https://www.satiengg.in/images/Electronics%20Department/BoS%20Dec%202024/4%20sem%20syllabus.pdf

Item 7	To propose a tentative pool of courses which can be opted for getting an (i) Honours Degree (for students of the parent department) (20 credits additionally to be earned between V to VIII sem.) (ii) Minor Degree (for students of other departments) (20 credits additionally to be earned between V to VIII sem.) <i>[These will be offered through SWAYAM/NPTEL/MOOC based Platforms for the B.Tech. V to VIII semester students (for the batch admitted in 2022-23)].</i>
Resolution	A tentative pool of courses available on NPTEL/SWAYAM was presented and approved for obtaining Honours and Minor Degree in Electronics and Communication Engineering. It has also been proposed that HoD / Departmental Committee can update the pool of courses offered to students as per the availability of courses offered through SWAYAM/NPTEL/MOOC based Platforms and permission from BOS members can be obtained retrospectively. Link for the pool of courses for Honours and Minor Degree: https://www.satiengg.in/honors-and-minors-etc

Item 8	To review the Scheme & Syllabus of B. Tech. Programme in Electronics and Communication Engineering (along with the Course Outcomes) for 2018 scheme.
Resolution	Syllabus and scheme applicable for 2018 batch (old scheme) are discussed and as the last batch of students (admitted under this scheme) was in final year thus it was decided to keep the scheme and syllabus of 2018 scheme intact.

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Item 9	To prepare and finalize the syllabus of courses to be offered (for the batch admitted in 2020-21) under Departmental Elective (DE) Course/ Open Category (in traditional mode)/ SWAYAM/NPTEL/MOOC based Platforms. NPTEL (Course weeks equivalent credit 8 weeks=2 credits, 12 weeks = 3 credits, NO credits for courses of less than 8 weeks) for B. Tech. V th and VIII th Semester (students admitted under 2018 scheme)
Resolution	A tentative pool of equivalent courses available on NPTEL/SWAYAM of Departmental Elective (DE) / Open Category (OC) offered in traditional teaching mode / SWAYAM/NPTEL/MOOC based platform was presented and approved for Credits equivalence have been presented and finalized. Subject and Credits equivalence has also been discussed for the 2018 Schemes batch.

Item 10	To review result analysis, curricula feedback from various stakeholders, etc. and its analysis and impact
Resolution	Result, curricula feedback from various stakeholders are presented and discussed. https://satiengg.in/images/Electronics%20Department/BoS%20Dec%202024/feedback%20and%20result%20analysis.pdf

Item 11	Any other points with the permission of chair (11.1) To re-look on revision / appropriation / Scrutiny / updating of COs, CO-PO mapping after the OBE workshop proposed to be organized during Feb/ March 2025. (11.2) To match the curriculum requirement (In Special Cases) for the Ex students or B-A group students, the equivalence for the subjects to be offered to student in that particular semester.
Resolution	(11.1) The updation of COs, CO-PO mapping can be done after the OBE workshop proposed to be organized during Feb/ March 2025 by dean academic office. Approval of updated COs, CO-PO mapping can be obtained in the subsequent BOS meeting. (11.2) BoS Members proposed that the departmental committee (comprising of Dean Academic, HOD and two faculty members) will be empowered to identify and propose equivalences of subjects for such students with the approval of Director.

Conclusion: As per the BOS Meeting agenda, all the above mentioned points have been presented and discussed of Scheme & Syllabus of B.Tech (Electronics & Communication Engineering) course.



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As per the above comments and suggestions from BoS members, the existing syllabus and proposed new scheme and Syllabus have been updated and approved by the BoS Members.

The meeting concluded with Vote of Thanks from Dr. Ashutosh Datar, Head (Dept. of Electronics Engg.) & Chairman, to all the external subject experts and faculty members who have attended the meeting.

Dr. Vandana Vikash Thakre,
Associate Professor,
Department of ECE,
MITS Gwalior

Dr. Anand Agrawal,
Assistant Professor,
Department of ECE, IIT Kota

Mr. Ashutosh Patrikar,
Senior Manager,
EY, Pune

Dr. Ashutosh Datar
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Department of Electronics

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