BURN TECHNOLOGICAL		SAI (A Govt. Aide	SAMRAT ASHOK TECHNOLOGICAL INSTITUTE VIDISHA (M.P.) (A Govt. Aided UGC Autonomous & NAAC Accredited Institute Affiliated to RGPV Bhopal)													
and the	<u>e</u>				Scheme of E	xamina	tion (Semes	ter-III)							
VIDISHA M.P.					for Batch Ad	mitted	in ses	sion - 2	2022-23							
		Bac	helor	of Tec	hnology (B. To	ech.) – (CSE(I	Blockc	hain) (D	ept. of CS &	k IT)					
					Maxir	num M	arks A	Allotte	d		Cont	act Hr	s. per			
Subject	Subject	Subject Name			Theory]	Practi	cal	Total		week		Total		
Code	Category		ES	MS	Assignment	Quiz	ES	LW	Quiz	Marks	L	T	Р	Credits		
MAB 201	BSC	Discrete mathematics	60	20	10	10				100	3	1	0	4		
BCC 202	DC	Analysis and Design of Algorithms	60	20	10	10	30	10	10	150	3	0	2	4		
BCC 203	DC	Object Oriented Programming	60 20 10 10 30 10 10 150 3 0 2										4			
BCC 204	DC	Operating system	60	20	10	10	30	10	10	150	3	0	2	4		
BCO 205	OC	Open elective-1	60	20	10	10				100	3	0	0	3		
BCL 206	DLC	Internet Programming					30	10	10	50	0	0	4	2		
ILT 208	ILC	Internship-I (60 Hrs) Institute Level (Evaluation)					50	-	-	50	-	-	2	2		
Total 300 100 50 50 170 40 40 750 15 1 12											12	23				
ILC 200	ILC	Extracurricular Activities		1	It is a one cre	dit per	year a	ctivity	to be en	dorsed in eig	ht semes	ter mai	ks sheet			
MAC 207	MAC*	Energy, Ecology, Environment & Society												Grade		
HUM 209	HEC	Holistic Education Course												Grade		
MST: Minim	um two mid seme	ester tests to be conducted during Seme	ester*	MAC	and HEC cours	es class	es wil	l be co	nducted	in off hours	(Weeker	ıds)				



SAMRAT ASHOK TECHNOLOGICAL INSTITUTE (Engineering College), VIDISHA M.P. (An Autonomous Institute Affiliated to RGPV Bhopal) Bachelon of Tachnology P. Tach in CSE (Blackbain)

UIDISHA M.P.		1	Bacheloi	r of Tec	chnolog	gy B.Tec	h in CSE (Blo	ockchai	i n)						
Semester	/Year	III/II	Prog	gram		ect B.Tech – CSE(Blockchain)									
Subject Category	DC	Subject Code:	BCC	C-202	Sub Nan	ject ne	Analysis and	Desig	n of Alg	gorith	ms				
Maximu	n Marks A	llotted	·	1				Con	tact Ho	urs	Total				
Theory			1	Prace	tical	1	Total	Con			Credits				
ES		Assignment	Quiz	ES		Quiz	Marks			<u>P</u>					
60	20	10	10	30	10	10	150	3	0	2	4				
Prerequi	sites:														
Fundame	ntals of Dat	a structures.													
Course C	bjective:														
A) D	etermine di	fferent time comple	xities of	a given	algorit	hm									
B) D	emonstrate	algorithms using va	rious des	sign tecl	hniques	S									
C) D	evelop algo	orithms using variou	s design	techniq	ues for	a given p	problem.				II				
UNITS				Desc	ription	.S					Hrs.				
	Algorithm	s: Definition and ch	aracteris	tics. An	alysis:	Space an	nd Time Comp	lexity,	Asymp	totic					
	Notations,	Time Complexit	ty Analy	ysis of	algo	rithms (Linear Search	h, Inse	ertion	Sort					
Ι	Notations, Time Complexity Analysis of algorithms (Linear Search, Insertion Sort etc.)Recursive algorithms and recurrence relations. Solutions of recurrence relations. Divide and conguer technique, analysis, design and comparison of various algorithms based on this.														
	and conquer technique, analysis, design and comparison of various algorithms based on this technique, example binary search, quick sort, merge sort, Heap Sort, Strassen's matrix														
	technique, example binary search, quick sort, merge sort, Heap Sort, Strassen's matrix multiplication with their complexity analysis. s.														
	Greedy Algorithms: Knapsack problem Job sequencing with deadlines optimal merge														
	Greedy A	lgorithms: Knapsa	ck probl	lem, Jo	b sequ	uencing	with deadline	s, opti	mal m	erge					
II	patterns, F	Huffman coding, Dy	namic P	rogram	ming: I	viultistag	ge Graph, all pa	airs sho	Trovo	aths,	8				
	v-1 Kliap	sack, Chamed mai	ITA MUI	ipiicatio	on, Lo	ngest co	subseq	luence,	Trave	iiiig					
	salesperso	n problem.													
	Graph and	Tree Algorithms:	Fraversal	algorit	nms: D	epth Firs	t Search (DFS)) and B	readth	First					
ш	Search (B	SFS); Shortest path	algorith	ms- Di	ijkstra'i	s Algori n'a and	thms and Con	nplexit	y Anal	ysis,	8				
111	complexit	v analysis. Union	Find I	Data S	tructure	e. Topol	logical sorting	goritim z. Netv	work I	Flow	0				
	Algorithm					-, <u>r</u>									
	Branch &	Bound technique:	Definitio	n and	annlies	tion to s	solve 0/1 Kna	nsack I	Problem	1 8-					
IV	nuzzle pro	blem travelling sa	lesman n	roblem	Back	tracking	concept and i	ts exan	nnles li	ke 8	8				
	Queens's	problem, Hamiltonia	an cycle,	Graph (Colorin	ig proble	m.		-p		0				
		11, 11, 11	1 0	. 1	•1•				1 /	1.10					
V	hard Intr	and intractable Prot	vimation	mputat	illity of	Algorith	ims-P, NP, NF	'-comp	Peduc	1 NP	8				
v	techniques	S Lower bound theo	orv and its	s use in	solving	algebra	ic problem	is and	Keuu		0				
	teeninquet		iy und id		SOLVIN	5 uigeoiu	le problem.								
Total Ho	urs										40				
Course O	utcomes:														
CO1: Exp	lain the inh	erent mechanism in	volved in	function	oning o	f an oper	ating system. I	Differer	ntiate ar	ıd just	ify the				
need of value $CO2$: And	arious opera	ating systems.	mee with	their o	ompori	sons									
CO2. And $CO3$ · And	alyse variou	s synchronisation te	chniques	with th	eir con	nnarisons	s derive the solu	ution fo	or deadl	ock si	tuation				
CO4: Des	CO4: Describe memory management system of an operating system. Analyse and compare various management														
schemes.	schemes.														
CO5: Des	cribe and A	analyze File and Dis	k manage	ement T	echniq	ues.									
Text Bool	K:-		D	110.		G1'6 1 6			. 1 .	.1 **					
• 1	nomas Cori	men. Charles Leiser	son. Kona	ald Rive	est and	Cliford S	stein, "Introduc	ction to	Algori	thms".	PHI. 3rd				

edition, ISBN-13: 978-8120340077

• Ellis Horowitz, Sartaj Sahni and SanguthevarRajasekaran, "Fundamentals of Computer Algorithms", Universities Press, 2nd edition (2008), ISBN-13: 978-8173716126

Reference Books:-

• Gilles Brassard and Paul Bratley, "Fundamentals of Algorithmics", PHI, ISBN-13: 978- 8120311312

List/Links of e-learning resource

https://archive.nptel.ac.in/courses/106/106/106106131/

Modes of Evaluation and Rubric

The evaluation modes consist of performance in two mid semester Tests, Quiz/Assignments, term work, end semester theory and practical examination.

CO-PO Mapping:

r		1	1	1	1	1	1	-		1			1	
COs	PO_1	PO ₂	PO ₃	PO ₄	PO ₅	PO ₆	PO ₇	PO_8	PO ₉	PO_1	PO ₁₁	PO ₁₂	PSO1	PSO2
CO-1	3	3	2	3	1							2	3	
CO-2		3	3	2	3									
CO-3	2	3	3	3	2									
CO-4		2	3	3										
CO-5		3	2	3										

Suggestive list of experiments:

Q.1 Understand the working of Ubuntu operating system and basic commands for implementing Algorithm in c programming in Ubuntu operating system using gcc compiler.

Q.2 Write a simple c program to add two integer numbers.

Q.3 Implement factorial of given number using iteration method and recursive Method.

Q.4 Implement logic to swap two integer number using three different approach.

Q.5 Implement Algorithm to determine given number is divisible by 5 or not without using % Operator.

Q.6 Implement Algorithm to convert binary number to decimal number without using array and Power function.

Q.7 Implement Algorithm to print reverse of string using recursion and without using characterArray.

Q.8 Implement Linear Search Algorithm.

Q.9 Implement Binary Search Algorithm (By using Iterative Approach)

Q.10 Implement Binary Search Algorithm (By using Recursive Approach)

Q.11Implement Insertion Sort Algorithm

Q.12 Implement Quick Sort Algorithm (By using Recursive Approach)

Q.13 Implement Quick Sort Algorithm (By using Non Recursive Approach).

Q.14 Implement Merge Sort Algorithm.

Q.15 Implement Heap Sort Algorithm.

Recommendation by Board of studies on

Approval by Academic council on

Compiled and designed by

Subject handled by department

Department of CS & IT



SAMRAT ASHOK TECHNOLOGICAL INSTITUTE (Engineering College), VIDISHA M.P. (An Autonomous Institute Affiliated to RGPV Bhopal) Bachelor of Technology B.Tech in CSE (Blockchain)

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Semester	nester/Year III/II Program B.Tech – CSE(Blockchain)														
Subject	DC	Subject Code:	BCC	-203	Sub	ject	Object Orien	ted Pro	ogrami	ming					
Category					Nan	ie	J	1	8						
Maximur	n Marks A	lotted		Duca	tiaal		T-4-1	Cont	tact Ho	urs	Total				
T neory	MG	Aggignmont	Ouiz	Frac		Ouiz	_ 10tai Morks	T	Т	D					
ES 60	20			20 20				3	1	r	1				
00	20	10	10	50	10	10	130	5	U	4					
Prerequis	sites:														
Fundamer	ntals of prog	ramming skills.													
Course O	bjective:														
A) Enable as a vehic	e students to le.	understand concep	ts and pri	nciples	s of obj	ect orien	ted programmi	ng metl	nodolog	gies us	ing JAVA				
B) Also le	earn softwar	e development and	problem s	solving	gusing	this JAV.	A technology.								
UNITS Descriptions															
	Introduction: Procedural Paradigms of programming, Object Oriented Paradigm for														
Introduction: Procedural Paradigms of programming, Object Oriented Paradigm for programming, Procedural vs. Object Oriented Programming, Principles of OOP, Benefits and applications of OOP. OOP Concepts: Data Abstraction, Encapsulation, Inheritance and Polymorphism. Introduction of Java, Features of Java, Byte Code and Java Virtual Machine, Leep Development Kit (IDK)															
	Polymorphism. Introduction of Java, Features of Java, Byte Code and Java Virtual Machine, Java Development Kit (JDK).														
II Command Line Argument, Classes and Objects, Encapsulation, Tightly Encapsulated classes, Nested class, Inner class, Anonymous inner class. inbuilt classes: Object, String, StringBuffer, Array, Vector. Wrapper classes. Data members, member Function, Data Hiding: Visibility modifiers in java.															
III	Is-A relation class, Meth static keyw return type control floo	onship, Has-A relat nod Signature. Over vord, finalize() met e. Super, final key w.	ionship, I cloading, (hod, Cast word, ove	nherita Constru ing ob erloadi	ance in uctor O jects, In ng vs.	Java, typ verloadir nstance o overridir	bes of inheritan ng, Method Ove f operator, Ove ng. Static cont	ce, Sup erloadin erriding rol flov	per and ng, this g, covar w, insta	sub and riant ance	8				
IV	Abstraction defining a extending CLASSPA	n: Abstract class, n interface, imple interfaces. Defir TH, importing pacl	Interface menting ting, Cre cages. Co	in Jav interfa eating upling,	va, diff ce, app and Cohes	erences blying in Accessin ion.	between class terfaces, varia g a Packago	es and bles in e, Uno	interfa interf derstand	ices. `ace, ding	8				
VException Handling: Concepts of Exception handling, types of exceptions, usage of try, catch, throw, throws and finally keywords, Built-in exceptions, creating own exception subclasses. Multithreading: Concepts of Multithreading, differences between process and thread, thread life cycle, creating multiple threads using Thread class, Runnable interface. Synchronization, thread priorities, inter thread communication, daemon threads, deadlocks, thread groups.															
Total Hours															
Course Outcomes:															
CO1. D. (č	abiaata	f a -1.		ation 1		a theorem								
CO2: Des CO3: Des	sign java app sign java app	objects, members of plication using OOI plication using cons	Ps princip	ind rela les. overloa	ationshi ading ar	nd overrie	g them . ling concepts.								

CO3: Design Java application using constructors, overloading **CO4**: Demonstrate package creation and exception handling.

CO5: Understand and develop multithreaded application programs.

Text Book:-

- Naughton & Schildt, "The Complete Reference Java 2", TataMcGraw Hill
- E Balaguruswamy, "Programming in Java", TMH Publications

Reference Books:-

- Deitel "Java-How to Program:" Pearson Education, Asia
- Horstmann & Cornell, "Core Java 2" (Vol I & II), Sun Microsystems
- Ivan Bayross, "java 2.0", BPB publications

List/Links of e-learning resource

https://archive.nptel.ac.in/courses/106/105/106105153/

Modes of Evaluation and Rubric

The evaluation modes consist of performance in two mid semester Tests, Quiz/Assignments, term work, end semester theory and practical examination.

CO-PO Mapping:

COs	PO ₁	PO_2	PO ₃	PO ₄	PO ₅	PO ₆	PO ₇	PO_8	PO ₉	PO ₁	PO ₁₁	PO ₁₂	PSO1	PSO2
CO-1	3	1	1									3	3	3
CO-2	3	1		1	2							2	1	3
CO-3	3	2	1									2	2	1
CO-4	3	3	2	3	2	1			1	2		3		3
CO-5	3	3	3	2	1				2		2	2	3	

Suggestive list of experiments:

- 1. Write a program to display any message.
- 2. Write a Java program to display the default value of all primitive data types of Java.
- 3. Write a program to give an example of control statements.
- 4. Write a program and give an example for command line arguments.
- 5. Write a program to create a room class, the attributes of this class is room no, room type, room area and A machine. In this class the member functions are set data and display data..
- 6. Write a program to create a class 'simple object'. Using the constructor display the message.
- 7. Write a program to give the example for 'this' operator. And also use the 'this' keyword as return statement.
- 8. Create a class named as 'a' and create a subclass 'b'. Which is extends from class 'a'. And use these classes in 'inherit' class.
- 9. Write a program to give an example of method overloading and overriding concepts.
- 10. Write a program to give a simple example for abstract class.
- 11. Write a program to give example for multiple inheritances in Java.
- 12. Write a program to illustrate usage of try/catch with finally clause.
- 13. Write a program to create two threads. In this class we have one constructor used to start the thread and run it. Check whether these two threads are run are not.

Recommendation by Board of studies on	
Approval by Academic council on	
Compiled and designed by	
Subject handled by department	Department of CS & IT



SAMRAT ASHOK TECHNOLOGICAL INSTITUTE (Engineering College), VIDISHA M.P. (An Autonomous Institute Affiliated to RGPV Bhopal) Bachelor of Technology B.Tech in CSE (Blockchain)

VIDISHA M.P.	N 7		Bachelor	01 1 60	cintolog	gy в.тес			<u>n)</u>					
Semester	/Year	111/11	Prog	ram	0.1	• • • •	B.Tech – CSI	(Block	<u>cchain</u>)				
Subject	DC	Subject Code:	BCC	204	Sub	ject	Operating sys	stem						
Maximu	n Marks A	 lotted			Inan	le					Total			
Theory		noticu		Prac	tical		Total	Cont	act Ho	urs	Credits			
ES	MS	Assignment	Oniz	ES	LW	Ouiz	Marks	L	Т	Р				
60	20	10	10	30	10	10	150	3	0	2	4			
	-		-		-	-		1 -		1				
Prerequi	sites:													
knowledg	ge of compu	iter fundamentals												
Course O	bjective:													
To unders	stand operat	ing system architect	ure and f	unction	ning alo	ng with i	n-depth knowle	edge of	interna	als and	l working			
of OS mo	dules like p	rocess management,	, Storage	manag	ement,	file syste	m, security and	l protec	tion.					
UNITS			. ~	Desc	ription	S					Hrs.			
	Overview-	Introduction to Ope	rating Sy	stems,	Evoluti	ion of Op	erating System	mainfi	ame,					
	desktop, m	ultiprocessor, Distr	ibuted, N	etwork	Operat	ing Syste	em, and Cluster	red and	Handh	eld				
Ι	System), C	Typerating System St	ructure- C	Deration Deration	ng Syst	em Servi	ces and System	i Calls,	Systen	n	8			
	Multiprogramming, time-sharing system and Distributed Operating systems, Objectives and													
	functions	of OS	ing system	ii ana i	J 15t110t	ited oper	ating systems,	objecti	ves and	*				
	Process M	anagement-Concept	. Process	Contro	ol Blocl	s (PCB)	Process Sched	luling.	Schedu	ling	0			
II	Criteria, Scheduling Algorithms and their evaluation. Threads Overview and Multithreading													
	Inter Proce	esses Communicatio	n and Cri	itical S	ection l	Problem a	and Solution-Se	emapho	res and	1				
III	Monitors, Deadlock Characterization, Methods for deadlock handling, deadlock prevention,													
	deadlock avoidance, deadlock detection and recovery from deadlock.													
	Storage Management-Memory Hierarchy, Concepts of memory management, MFT and MVT,													
	logical and physical address space, swapping, contiguous and non- contiguous allocation,													
IV	Paging and	d Segmentation Stru	cture and	Implei	mentati	on of Pag	ge table, Virtua	1 memo	ory, Cao	che	8			
	Memory C	organization, Demar	ia paging	, Page	replace	ment Alg	gorithms. Thras	ning, D	emana					
	File and D	ion isk Management-Fil	le concen	ts Acc	ess me	thods Di	rectory Structu	re File	Sharin	σ				
V	and Protec	tion. Free space ma	nagement	Disk S	Schedul	ling. Effi	ciency and Perf	ormanc	ce- Cas	e l	8			
	study on U	Jnix, Linux and Win	dows.	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,							Ũ			
Total Ho	urs	,									40			
Course O	utcomes:									·				
CO1: Exp	plain the inh	nerent mechanism in	volved in	functi	oning c	of an oper	rating system. I	Differer	ntiate a	nd just	tify the			
need of va	arious opera	ting systems.												
CO2: Ana	alyze variou	is scheduling technic	ques with	their c	ompari	sons.								
CO3: Ana	alyze variou	is synchronization te	chniques	with t	heir coi	nparison	s derive the sol	ution fo	or dead	lock s	ituation.			
cO4: Des	scribe memo	ory management sys	stem of ar	1 opera	ting sys	stem. Ana	alyze and comp	are va	irious r	nanag	ement			
CO5. Des	scribe and A	nalyze File and Dis	k manage	ement 7	Fechnic	mes								
Text Boo	k:-	indigize i ne una Dis	k manage		leenne	ues.								
• P	eterson, J.L	. & Silberschatz, A.:	Operatin	g Syste	em Con	cepts. Ad	ldison. Weslev	Readir	lg.					
• B	rinch, Hans	en: Operating Syste	em Princi	ples, P	rentice	Hall of Iı	ndia.		0					
Referenc	e Books:-	1 0 1												
• H	aberman, A	.N.: Introduction to	Operatin	g Syste	m Des	ign Galgo	otia Publication	, New 1	Delhi.					
• T	anenbaum,	A.S.: Operating Sys	tems.	-		2								
• H	ansen, P.B.	: Architecture of Co	ncurrent	Program	ms, PH	I.								
• S	haw, A.C.:	Logic Design of Op	erating Sy	ystems,	, PHI									
List/Link	s of e-learn	ing resource												
https://archive.nptel.ac.in/noc/courses/noc16/SEM2/noc16-cs10/														
Modes of	Evaluation	n and Rubric												

The evaluation modes consist of performance in two mid semester Tests, Quiz/Assignments, term work, end semester theory and practical examination.

CO-I	$\begin{array}{c c c c c c c c c c c c c c c c c c c $															
	COs	PO ₁	PO ₂	PO ₃	PO ₄	PO ₅	PO ₆	PO ₇	PO ₈	PO ₉	PO ₁	PO ₁₁	PO ₁₂	PSO1	PSO2	
	CO-1		2			2							2	1	2	
	CO-2	2	3		2	1						1	2	3	3	
	CO-3	2	3	3	2								2	2	2	
	CO-4	2	2		2								2	3	3	
	CO-5	2	2	2									2	3	3	
Sugg	estive lis	st of ex	perim	ents:												
1. Im	1. Implementation of Basic Linux Commands.															
2. Im	2. Implementation of Process Related System Calls (Fork).															
3. W1	3. Write a program to simulate the following non-pre-emptive CPU scheduling algorithms to find turnaround															
time	time and waiting time. a) FCFS b) SJF															
4. W1	rite a pro	gram t	o simul	late the	e follov	ving Cl	PU sch	eduling	g algori	ithms t	o find	turnarou	and time	e and wa	iting	
time.	a) Roun	d Robi	n b) Pr	iority												
5. W1	rite a C p	rogran	n to sin	nulate	page re	placen	nent alg	gorithn	ns a) FI	FO b)	LRU c) OPTI	MAL			
6. W1	rite a pro	gram t	o simu	late Ba	nkers a	algorith	nm for	the pur	pose of	f deadl	ock av	oidance				
7. Write a program to simulate disk scheduling algorithms a) FCFS b) SCAN c) C-SCAN																
Recommendation by Board of studies on																
Appr	oval by A	Acaden	nic cou	ncil or	1	_				_						_
Com	piled and	l design	ned by													
Subje	Subject handled by department Department of CS & IT															



SAMRAT ASHOK TECHNOLOGICAL INSTITUTE (Engineering College), VIDISHA M.P. (An Autonomous Institute Affiliated to RGPV Bhopal) Bachelor of Technology B Tech in CSE (Blockchain)

Bachelor of Technology B.Tech in CSE (Blockchain)																
Semester/	lear	III/	Π		Pr	ogran	1			B.Tech	i – CSE	Block	chain)		
Subject Category	DLC	Sub	ject Co	ode:	BC	CL-206	5	Subjeo Name	t	Interne	t Progra	ammin	g	/		
Maximum	Marks	Allotte	d									0			Tot	tal
Theory						P	ractic	al		Total		Cont	act Ho	urs	Cre	edits
ES	MS	A	ssignn	ient	Quiz	E	S I	W 0	Quiz	Mark	KS	L	Т	P		
						30) 1	0 1	0	50		0	0	4	2	
Prerequisi	tes:															
Fundament	al know	ledge of	f progra	amming												
Course Of	jective:															
Understand	static a	nd dyna	mic we	b pages	5.											
UNITS	UEDOI		LCC N	7.1 15		D	escrip	tions	1.0	•		1 7 .			H	rs.
Ι	MEBSII	E BAS	lCS, W ls, Wor	eb Esse d wide	entials web.	: Clier	its, Sei	rvers a	nd Con	nmunica	ition, T	he Inte	rnet, B	asıc		8
	HTTP R	equest l	Messag	e, HTT	P Resp	onse l	Messa	ge, We	b Clier	nts, Web	Servei	s, HTN	AL5,			0
II	Video control															8
	Video control III CSS3, Inline, embedded and external style sheets, Rule cascading, Inheritance, Backgrounds,															
III CSS3, Inline, embedded and external style sheets, Rule cascading, Inheritance, Backgrounds, Border Images, Colors Shadows, Text, Transformations, Transitions, Animations.															8	
Border Images, Colors Shadows, Text, Transformations, Transitions, Animations. Java Script: An introduction to JavaScript, JavaScript DOM Model-Date and Objects, Regular														lar		
IV Java Script: An introduction to JavaScript, JavaScript DOM Model-Date and Objects, Regular Expressions.															8	
Expressions. V Exception Handling-Validation-Built-in objects-Event Handling-DHTML with JavaScript. XML- Elements, attributes, parser, DOM, query.																8
XML- Elements, attributes, parser, DOM, query. Total Hours															0	
Course Outcomes:															rU	
Course Outcomes: CO1: To understand and interpret the basic concepts of the Internet, tools. CO2: To understand, analyze CSS components and apply them to web page design tools like HTML,CSS. CO3: To know and analyze client side scripting language concepts. CO4: Design and Develop Internet applications with the help of Java script. CO5: Understand the concept of exceptional handling Text Book:																
Text Dook		-11 1 - A	41 IZ -1	4 . !! \ \ 7	1. T 1				X7 - 1- / T -	D	·		<u>C11</u>			
• AC	nyut Go	ubole,A	Edition	hate" we		nnolog	gies: I o	_P/IP,	Ned/Ja	va Prog	rammir	ig, and	Cloud			
Co	mputing	,1mra	Editio	i,McGr	aw nii	I Eau	cation.									
Reference	Books:-															
• De 20 • Ra	itel, Deit)6. Kamal,	tel, Gol	dberg, '	'Interne Web Te	et & W	orld W	Vide W , Tata	/eb Ho McGra	w to Pr w-Hill	rogram"	, Third	Editio	n, Pear	son E	ducati	ion,
List/Links	of e-lea	rning r	esourc	e												
• httj	s://archiv	ve.nptel.	ac.in/no	c/course	s/noc1	6/SEM	2/noc1	6-cs10/								
Modes of]	Evaluati	on and	Rubri	c												
The evalua	tion mod	les cons	sist of p	erforma	ance in	, Quiz	z/Assig	gnment	s, term	work, e	end sem	lester p	ractica	l exar	ninati	on.
CO-PO Mapping:																
CO	PO ₁	PO ₂	PO ₃	PO ₄	PO ₅	PO ₆	PO ₇	PO ₈	PO ₉	PO ₁	PO ₁₁	PO ₁₂	PSO	1 P	SO2	
CO	1 2	1	2										1		1	
CO	2 2	1	2										1		1	
CO	3 2	1	2					1	-				1	_	2	
CO	<u>4 2</u>	2	2										1		2	
	$\frac{5}{1}$	2	2												2	
Suggestive	list of e	xperim	ents:													

1. Design a web page to display your CV.

2. Design a web page using HTML tags to take the input in a form and display it in another page/frame.

3. Design a web page to isolate a part of the text that might be formatted in a different direction from other text outside it

4. Create a Zebra Striping a Table and make an image rounded with CSS3.

5. Create speech bubble shape and Image cross effect with CSS3 transition.

6. Using HTML, CSS create a styled checkbox with animation on state change.

7. Using HTML, CSS create display an image overlay effect on hover.

8. Using HTML, CSS create a list with floating headings for each section.

9. Using HTML, CSS, JavaScript create a typewriter effect animation.

10. Using HTML, CSS create an animated underline effect when the user hovers over the text.

11. Write a JavaScript program to set paragraph background color.

12. Write a JavaScript function to add rows to a table.

13. Write a JavaScript function that accepts a row, column (to identify a particular cell) and a string to update the cell's contents.

14. Write a JavaScript program to highlight the bold words of the following paragraph, on mouse over a certain link.

15. Write a JavaScript program to get the window width and height (any time the window is resized).

Recommendation by Board of studies on	
Approval by Academic council on	
Compiled and designed by	
Subject handled by department	Department of CS & IT

Open Courses launched by Program are not applicable for students of parental program.

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Open Course Offered by CSE(BC) Session: 2023-24 Semester III												
Open Course-I (BCO-205)	Α	B	С									
	Computer System Organisation	Data Structure	Operating system									
Prerequisite	Digital Electronics	C/C++ Programming	Computer fundamentals									
Remark	Open to All	Not Applicable for - CSE and Allied branches, EC	Not Applicable for -									
			CSE&AIADS									



SAMRAT ASHOK TECHNOLOGICAL INSTITUTE (Engineering College), VIDISHA M.P. (An Autonomous Institute Affiliated to RGPV Bhopal)

Bachelor of Technology B.Tech in CSE (Blockchain) Semester/Year Ш Program B.Tech - CSE(Blockchain) Subject Subject OC Subject Code: BCO-205(A) Computer system organization Category Name **Maximum Marks Allotted** Total **Contact Hours** Theory Practical Credits **Total Marks** LW Ouiz ES MS Assignment Ouiz ES \mathbf{L} Т Р 60 20 10 100 3 0 0 3 10 -------**Prerequisites:** Fundamental knowledge of digital electronics. **Course Objective:** Understand the organization and architecture of computer systems and electronic computers. Design a simple computer using hardwired and micro programmed control methods. Study the basic components of computer systems besides computer arithmetic. Understand input-output organization, memory organization and management, and pipelining. UNITs Descriptions Hrs. Introduction: Function and structure of a computer, Functional components of a computer, Interconnection of components, Performance of a computer, Register Transfer language : Register Transfer, Bus and Memory Transfers, Three-Stare Bus Buffers, Memory Transfer, Arithmetic Microoperations Binary Adder, Ι 6 Binary Adder-Subtractor, Binary incrementer, Arithmetic Circuit, Logic Microoperations, Shift Microoperations, Arithmetic Logic Shift Unit, List of Logic Microoperations, , Shift Micro operations, Arithmetic Logic Shift Unit Control unit: Control memory, address sequencing, micro program example, Microinstruction Format, Π Symbolic Microinstructions, The Fetch Routine, Symbolic Micro program and design of the control unit, 6 Microprogram Sequencer. CPU design: Instruction cycle, data representation, memory reference instructions, input-output, and 8 III interrupt, addressing modes, data transfer and manipulation, program control. Computer arithmetic: Addition and subtraction, floating point arithmetic operations, decimal arithmetic unit. Memory organization: Memory hierarchy, main memory, auxiliary memory, associative memory, cache IV memory, virtual memory; Input or output organization; Input or output Interface, asynchronous data 8 transfer, modes of transfer, priority interrupt, direct memory access Pipeline: Parallel processing, pipelining-arithmetic pipeline, instruction pipeline; Multiprocessors: V Characteristics of multiprocessors, interconnection structures, inter-processor arbitration, inter-processor 7 communication and synchronization. Total Hours 35 **Course Outcomes:** CO1:Understand the organization and levels of design in computer architecture. CO2: Describe Register transfer languages, arithmetic micro-operations, logic micro-operations, shift micro-operations address sequencing, micro program example, and design of control unit CO3: Understand the Instruction cycle, data representation, memory reference instructions, input-output, and interrupt, addressing modes, data transfer, and manipulation, program control. Addition and subtraction, floating point arithmetic operations, decimal arithmetic unit. CO4: Knowledge about Memory hierarchy, main memory, auxiliary memory, associative memory, cache memory, virtual memory Input or output Interface, asynchronous data transfer, modes of transfer, Priority interrupt, and direct memory access. CO5: Explore the Parallel processing, pipelining-arithmetic pipeline, instruction pipeline Characteristics of multiprocessors,

interconnection structures, inter-processor arbitration, inter-processor Communication, and synchronization.

Text Book:-

• M. Morris Mano, "Computer Systems Architecture", Pearson, 3rdEdition,2007.

Reference Books:-

- John D. Carpinelli, "Computer Systems Organization and Architecture", Pearson, 1stEdition, 2001.
- Patterson, Hennessy, "Computer Organization and Design: The Hardware/Software Interface", Morgan Kaufmann, 5
 - thEdition,2013

List/Links of e-learning resource

Modes of Evaluation and Rubric

The evaluation modes consist of performance in two mid semester Tests, Quiz/Assignments, term work, end semester practical examination.

CO-PO Mapping:

 $COs \quad PO_1 \quad PO_2 \quad PO_3 \quad PO_4 \quad PO_5 \quad PO_6 \quad PO_7 \quad PO_8 \quad PO_9 \quad PO_1 \quad PO_{11} \quad PO_{12} \quad PSO1 \quad PSO2$

	CO-1	1	1	2										1	2	
	CO-2	2	2	2										1	2	
	CO-3	2	1	2										1	2	
	CO-4	2	1	2											2	
	CO-5	2	2	1										1	2	
Suggestive	Suggestive list of experiments:															
Recommendation by Board of studies on																
Approval by Academic council on																
Compiled and designed by																
Subject has	ndled by a	lepartr	nent						Depa	ırtment	of CS	& IT				

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1 1G	TAT		(Engineering College), VIDISHA M.P.												
No. Company				(An A	utono	mous I	nstitute A	Affiliated to	RGPV	Bho	pal)				
The	Cel		DEPARTMENT OF CS & IT												
Semester	r/Year		III/II Program B.Tech – CSE(B									lockchain)			
Subjec Categor	t OC		Subject Code:	BC	CO-205	5(B)	Subje	ect Name	ame Data structure						
Catego	l y		Maximum	Marks A	Allotte	d			Can	4a a4 I	Laura	Tatal	Cuedita		
	T	heo	ory	<u> </u>		Practi	Total		Creatts						
ES			Assignment		ES		Quiz	Marks			P		2		
00															
Prerequisites:															
Logical thinking and Computer Fundamentals															
Course Objective:															
Intro	oduce the fu	ıdaı	mentals of data	structur	es										
• Utili	zation of the	e co	oncepts are usef	ul in pro	blem s	olving									
UNITs			1]	Descrip	otions						Hrs.		
	Problem-solving concepts: top-down, bottom-up design, Concept of data type, variable, constant,														
	and pointer	rs. I	Dynamic memo	ry alloc	ation. A	Algorith	m: Defin	ition and cor	nplexit	y Ana	ılysis.				
I	Introduction to data structure: Linear, Nonlinear, Primitive, and Nonprimitive. Arrays-Concepts of												6		
	Arrays, Single dimensional array, two-dimensional array- Representation and Address Calculation,														
	Uperations on arrays with algorithms (traversing, searching, inserting, deleting).														
	List-Singly linked lists: Representation in memory, Operations on singly linked list with														
II	algorithms traversing, searching, insertion, deletion)Doubly linked list- Operations with algorithms and analysis Circular linked lists-Operations with algorithms and analysis Representation from the second secon														
	manipulati	ons	of polynomial	s/sets us	ng linl	ced lists	aigeriain 5.	no una unary	515. 10	prese	intatioi	r coump,			
	Stack- Intr	odu	action to Stack	and its o	operatio	ons, Im	plementa	tion of stack	using	array	and li	nked list			
	with comparison. Application of stacks (Polish Notations, converting infix to postfix notation,														
III	evaluating postfix notation, Parenthesis balancing, Recursion).												8		
	Queue- Int	rod	luction to Quei	e and it	s opera	ations.	Impleme	ntation of qu	ieue us	ing ai	ray an	d linked			
	list. De-qu	eue,	, circular queue	, priorit	/ queue	e. Appli	cations o	t the queue.				. 1			
	tree Dell	nitio dor	on and termino	logy, th	e conc	ept of t	onary tre	e and repres	entation	n, Ira	versin	g binary Binary			
IV	Search Tre	es a	and Concept of	Balance	tree (A	AVL)	tii ali alg	301111111 -11150	2111011 6	ina a	ciction	. Dinary	8		
1,	Graph- Definition and terminology Types of graphs Representation of graph Traversing of the												0		
	graph- Breadth First Traversing and Depth First Traversing.														
	Searching-	Se	earch methods-	Linear	search	, Binar	y search,	and Hashin	ıg (coll	ision,	chain	ing, and			
V	probing) with their algorithms and analysis. Sorting- Sorting methods-Bubble sort, Selection sort,														
	Insertion s	ort,	Quick sort, Me	rge sort											
Total Hours											35				
Course	Jutcomes:	rahl	lem colving us	na data	etructu	re and a	various se	arching and	sorting	math	ode				
$CO2 \cdot \Lambda$	nnly differe	nt c	concepts of data	structu	es to s	olve dif	ferent co	mputing prol	bleme	, men	ous.				
	pply unicie		ss pattern of va	rious da	to struc	tures of	nd under	tond their on	nlicabi	lity					
	valuate and	Cor	ss patient of va	rmance	of diff	aront do	ita structu	stand then ap	vorld p	nty. robler	ng				
CO5: G	roph and Tr		structure with the	air oper	otions	and anr	licobility	1105 011 10ai-v	vonu p		115.				
Tart Day				ien opei	ations	anu app	Jiicabiiity	·							
Text Boo)К:-														
• Data S	tructure- Ho	rwit	tz and Sartaj Sa	hni											
Reference	e Books:-														
• Data S	tructure- Scł	naur	m's Series- Mc	Graw Hi	ll Publ	ication									
• Data S	tructure thro	ugh	n C, Yashwant	Kanekar	BPB	Publicat	tion.								
List/Lin	ks of e-learr	ling	z resource												
• h	ttps://archiv	e.nr	ptel.ac.in/noc/c	ourses/n	oc22/S	EM1/n	oc22-cs2	6/							
Modes o	f Evaluation	ı an	nd Rubric												
The eval	uation mode	es co	consist of perfo	rmance	in two	mid se	emester T	ests, Quiz/A	ssignm	nents,	term v	work, end	semester		

theory examination.															
CO-PO Mapping:															
COs	PO ₁	PO ₂	PO ₃	PO ₄	PO ₅	PO ₆	PO ₇	PO ₈	PO ₉	PO ₁	PO ₁₁	PO ₁₂	PSO1	PSO2	
CO-1	1	1	2										1	2	
CO-2	2	2	2										1	2	
CO-3	2	1	2										1	2	
CO-4	2	1	2											2	1
CO-5	2	2	1										1	2]
Recommendation by Board of studies on											• •			-	
Approval by Academic council on															
Compiled and designed by															
Subject handled by department							Dep	Department of CS & IT							



SAMRAT ASHOK TECHNOLOGICAL INSTITUTE

(Engineering College), VIDISHA M.P.

(An Autonomous Institute Affiliated to RGPV Bhopal)

DEPARTMENT OF CS & IT

Semester/	Year	III/II		Pro	gram		B.Tech – CSE(Blockchain)							
Subjec Categor	t oc	Subject Code:	BCO	-205(C)	Su N	bject ame	0	peratin	g Syst	System				
		Maximum	Marks Al	lotted				Con	tact Ho	t Hours Total				
EC	MG	Theory	0.1	0 : F		al Oʻ	Total Marks				Credits			
ES 60	20	Assignment		ES	LW	Quiz	100		1	P 0	3			
00	20	10	10		l		100	5	U	U	5			
Prerequisites:														
knowledge of computer fundamentals														
Course Objective:														
To understand operating system architecture and functioning along with in-depth knowledge of internals and working of														
OS modules like process management, Storage management, file system, security and protection														
UNITS			De	escriptio	ons					H	lrs.			
Ι	Overview-In desktop, m Handheld S Calls, Syste Multitasking Systems, Ob	ne, em ne, ng	7											
II	Process Management-Concept, Process Control Blocks (PCB), Process Scheduling.Scheduling Criteria, Scheduling Algorithms, and their Evaluation. Threads Overview and Multithreading.													
III	Inter Processes Communication and Critical Section Problem and Solution-Semaphores and Monitors, Deadlock Characterization, Methods for Deadlock handling, deadlock prevention, deadlock avoidance, deadlock detection and Recovery from Deadlock													
IV	Storage Management-Memory Hierarchy, Concepts of memory management, MFT and MVT, logical and physical address space, swapping, contiguous and non-contiguous allocation, Paging and Segmentation Structure and Implementation of Page table, Virtual memory, Cache Memory Organization, Demand paging, Page replacement Algorithms. Thrashing, Demand segmentation										7			
V	File and Disk Management-File concepts, Access methods, Directory Structure, File Sharing and Protection, Free space management, Disk Scheduling, Efficiency, and Performance- A case study on Unix, Linux, and Windows.									7				
Total Ho	irs										35			
Course O	utcomes:													
CO1: Exp	plain the inhe need for vario	rent mechanism invo us operating systems	ved in th	he funct	tioning	of an ope	erating system. I	Differen	tiate ar	nd justi	fy the			
CO2: An	alyze various	scheduling technique	es with the	neir con	nparisor	IS.								
CO3: An	alyze various	synchronization tech	niques w	vith thei	ir compa	arisons to	o derive the solu	tion for	the de	adlock				
CO4: De	scribe the me	mory management sv	stem of a	an oper	ating sv	stem. An	alyse and comp	are vari	ous ma	nagen	nent			
schemes.														
CO5: De	scribe and An	alyze File and Disk N	Aanagen	nent Te	chnique	s.								
I CAT DO														
 Peterson, J.L. & amp; Silberschatz, A.: Operating System Concepts, Addison, Wesley-Reading. Brinch, Hansen: Operating System Principles, Prentice Hall of India. Reference Books:-														

- Haberman, A.N.: Introduction to Operating System Design Galgotia Publication, New Delhi.
- Tanenbaum, A.S.: Operating Systems.
- Hansen, P.B.: Architecture of Concurrent Programs, PHI.
- Shaw, A.C.: Logic Design of Operating Systems, PHI.

List/Links of e-learning resource

• https://archive.nptel.ac.in/noc/courses/noc16/SEM2/noc16-cs10/

Modes of Evaluation and Rubric

The evaluation modes consist of performance in two mid semester Tests, Quiz/Assignments, term work, end semester theory examination.

CO-PO Mapping:															
COs	PO ₁	PO ₂	PO ₃	PO ₄	PO ₅	PO ₆	PO ₇	PO ₈	PO ₉	PO ₁	PO ₁₁	PO ₁₂	PSO1	PSO2	
CO-1		2			2							2	1	2	
CO-2	2	3		2	1						1	2	3	3]
CO-3	2	3	3	2								2	2	2	
CO-4	2	2		2								2	3	3	
CO-5	2	2	2									2	3	3	
Recommendation by Board of studies on															
Approval by Academic council on															
Compiled and designed by															
Subject h	andled by	y departn	nent					Department of CS & IT							